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PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Title: QUICKLY REACQUIRABLE LOCKS

Application No.: 10/669,948

Filed: September 24, 2003

Examiner: Unassigned

Group Art Unit: 2185

Atty. Docket No.: 004-8728

July 16, 2004

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- ☒ Form(s) PTO-1449 (1 pages), including copy(ies) of 4 reference(s).
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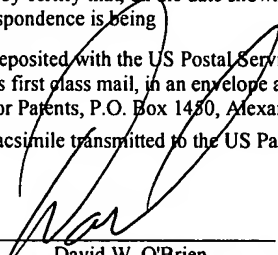
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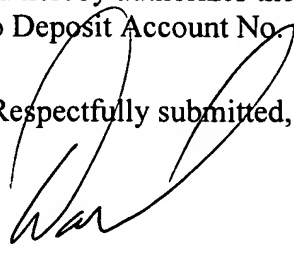
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David W. O'Brien

7/16/04
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Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8728
		Application No.: 10/669,948
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s): Dice et al.
		Filing Date: September 24, 2003
		Group Art Unit: 2185
		Date Submitted: July 16, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	File No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
.	1	Agesen, Ole et al., "An Efficient Meta-Lock for Implementing Ubiquitous Synchronization", ACM SIGPLAN Notices, Vol. 34, No. 10, pages 207-222, 1999.
t	2	Bacon, David F. et al., "Thin Locks: Featherweight Synchronization for Java", ACM SIGPLAN Notices, Vol. 33, No. 6, pages 258-268, 1998.
	3	Dice, David, "Implementing Fast Java™ Monitors with Relaxed Locks", In Proceedings of the USENIX JVM'01 Conference, 2001.
	4	Lamport, Leslie, "A Fast Mutual Exclusion Algorithm", ACM Transactions on Computer Systems, Vol. 5, No. 1, pages 1-11, 1987.
Examiner		Date Considered
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		